

6W, DC/DC Power Converter

SDA06G

SUMMIT
ELECTRONICS



Features

- Rated power: 6W Max
- Input voltage 40...160VDC
- Regulated output
- High efficiency up to 86%
- Isolation voltage 2250VDC
- Low ripple and noise
- Operating temperature range: -40 ~ +85°C ambient
- RoHS compliant
- Standard 1"x1" package
- Under voltage, over voltage, over current, and short circuit protection
- Meet IEC/EN 62368-1, EN50155 standards
- Designed for railway apps
- 3 year warranty

6W, DC/DC Power Converter

SDA06G



Part numbers

Model Number	Input Voltage [VDC]			V _{OUT} [VDC]	Output Current [mA]		Efficiency [%] Typ.	Capacitive Load [uF] Max.
	Nom.	Range	*Max.		Max	Min.		
SDA06G050	110	40-160	170	5.0	1200	0	80	1000
SDA06G120	110	40-160	170	12	500	0	84	470
SDA06G150	110	40-160	170	15	400	0	85	220
SDA06G240	110	40-160	170	24	250	0	86	100
SDA06G050D	110	40-160	170	±5	±600	0	80	470
SDA06G120D	110	40-160	170	±12	±250	0	84	100
SDA06G150D	110	40-160	170	±15	±200	0	85	100

* Input voltage exceed the Max. value may cause permanent damage.

* Only typical models are listed. Other models may be available upon request.

6W, DC/DC Power Converter

SDA06G



Electrical characteristics

Unless otherwise indicated, specifications are measured at $T_A=25^{\circ}\text{C}$, nominal input voltage, full load after warm up.

Parameter	Condition	Min.	Type	Max.	Unit	Note
Input current	Full load	-	68	-	mA	
Input current	No load		3	-	mA	
Reflected ripple current		-	25	-	mA	
Input voltage surge 1 second max		-0.7	-	180	VDC	
Startup input voltage	Full load	-	-	40	VDC	
Startup time	Resistive load	-	10	-	mS	
Input under voltage shutdown		28	33	-	VDC	
Remote On/Off control "Ctrl" pin open or logic high [ON] "Ctrl" pin grounded or logic low [OFF]	Logic high Logic low Ctrl pin current	3.5 0 -	- - 3	12 1.2 8	VDC VDC mA	Positive Logic
Output voltage accuracy	$I_{OUT}=5\%$ to 100%	-	± 1	± 3	%	
Line regulation Full load, $V_{IN}=V_{IN, Min}$ to $V_{IN, Max}$		-	± 0.5	± 1.0	%	
Load regulation $I_{OUT}=5\%$ to 100% of $I_{OUT, rated}$	Main output Others	-	± 0.5 ± 0.5	± 1.0 ± 1.5	%	
Cross regulation	Dual output models	-	-	± 10	%	

6W, DC/DC Power Converter

SDA06G



+I _{OUT} =50%, - I _{OUT} =25% to 100%						
Output ripple and noise	20MHz bandwidth	-	50	100	mVp-p	
Temperature coefficient	Full load	-	-	0.03	%/°C	
Dynamic load response I _{OUT} =25%~50%~75% of I _{OUT, rated}	Peak deviation* Peak deviation Recovery time	-	±3 ±3 300	±8 ±5 500	% V _{OUT} % V _{OUT} uS	*V _{OUT} =5V, ±5V
Output over voltage protection		110	-	160	% V _{OUT}	
Output over current protection		120	-	210	% I _{OUT}	
Output short circuit protection		Continuous, automatic recovery				
Input filter		PI filter				
Hot plug		None				

* Operating with less than 5% of rated load will not cause damage to the converters, but the performances data may not fall into the specifications, and stable operating is not assured.

6W, DC/DC Power Converter

SDA06G



General Specifications

Parameters	Condition	Min.	Typ.	Max.	Unit	Notes
Isolation voltage 1 minute, leakage current 1mA max.	I/P to O/P I/P & O/P to Case	2250 1600	-	-	VDC	
Isolation resistance Tested at 500VDC	I/P to O/P	1000	-	-	M ohm	
Isolation capacitance 100KHz, 0.1V	I/P to O/P	-	1000	-	pF	
Switching frequency*	Full load	-	300	-	KHz	PWM mode
Operating temperature	See "Derating Curve"	-40	-	85	°C	
Storage temperature		-55	-	125	°C	
Storage humidity	None condensing	5	-	95	%RH	
Pin soldering resistance 1.5mm away from case for 10 sec		-	-	300	°C	
Cooling method		Free air convection				
Case material		Aluminum alloy				
Vibration		IEC/EN61373 – Category 1, Grade B				
MTBF	MIL-HDBK-217F	>1,000,000 Hours, T _A =25°C				
Design based on standards		IEC/EN 62368-1, EN50155				

6W, DC/DC Power Converter

SDA06G



Safety certifications		IEC/EN 62368-1
EMC		CISPR32, EN55032 Class B, IEC/EN61000-4-2, 3, 4, 5, 6 EN50155, IEC/EN50121-3-2, EN55016-2-1
Size, and Weight		25.4 x 25.4 x 12 mm, 21g

* Switching frequency is measured at full load. The converter reduces the switching frequency at low load (less than 50% load) for better efficiency.

6W, DC/DC Power Converter

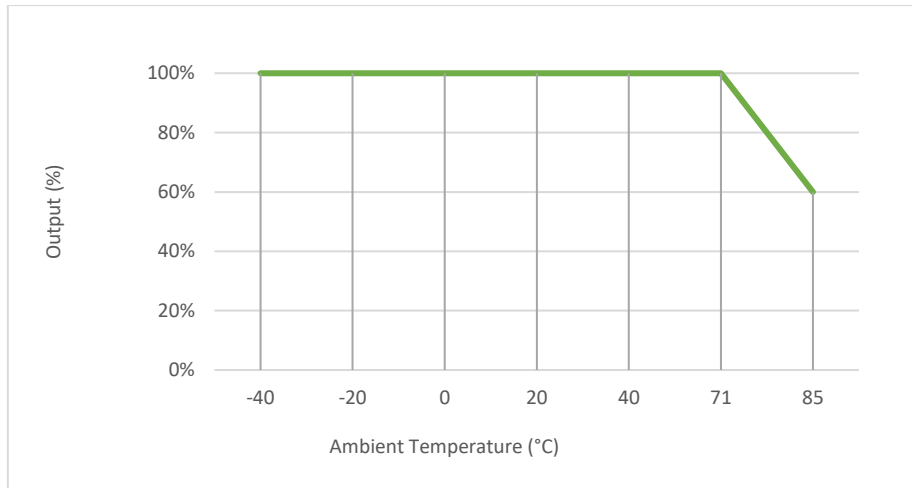
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Characteristics Curves

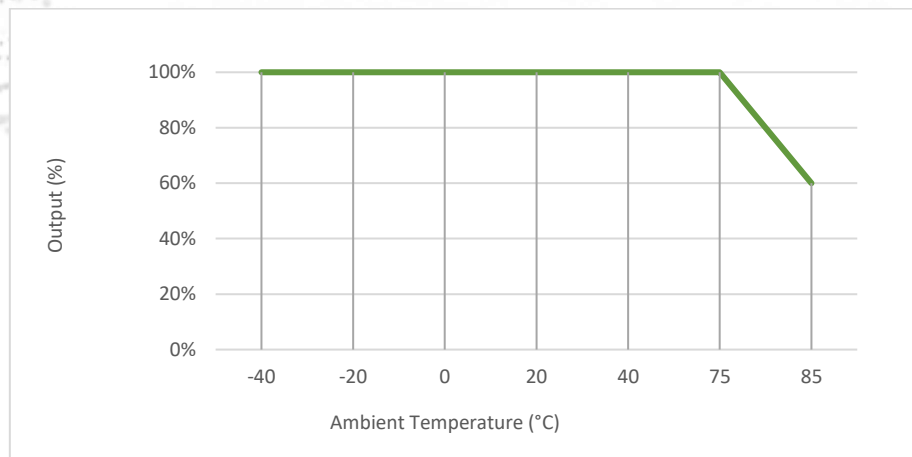
Derating Curve

Output vs Ambient Temperature



No heatsink

Output vs Input Voltage



With heatsink

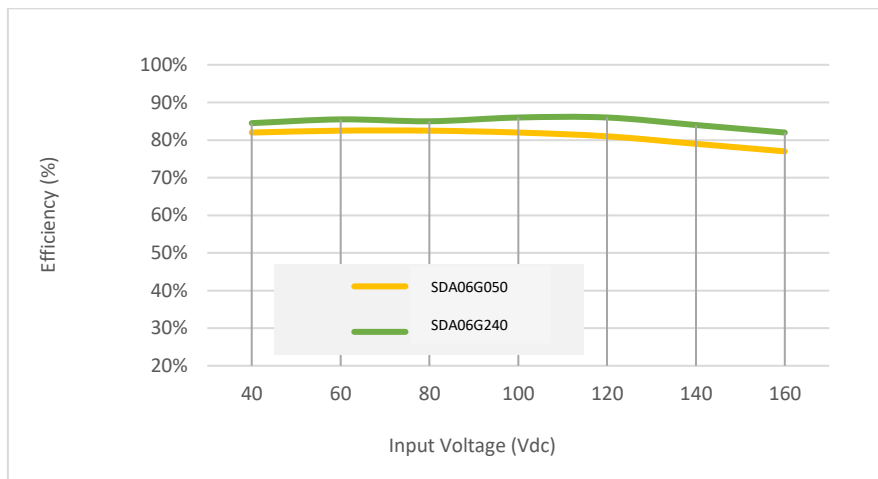
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SDA06G



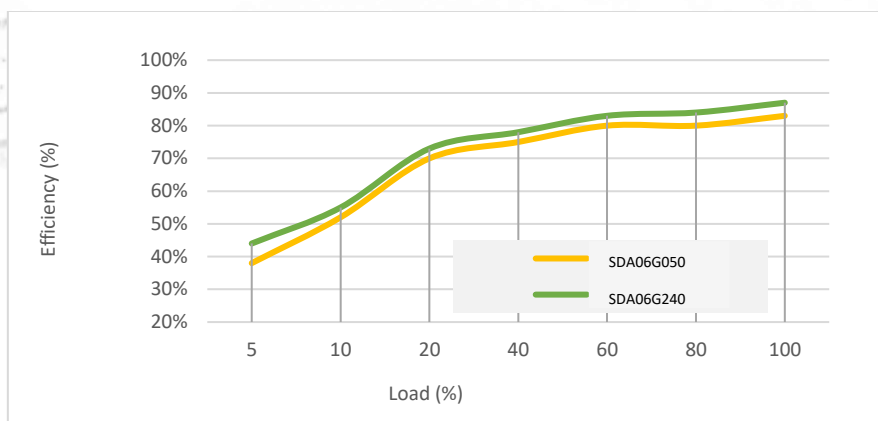
Efficiency Curve

Efficiency vs Input Voltage



Full load

Efficiency vs Load



$V_{IN}=110Vdc$

6W, DC/DC Power Converter

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Recommended External Circuit

Typical External Circuit

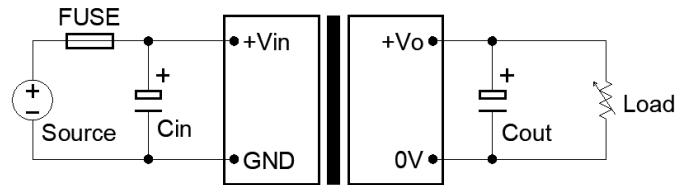


Figure 1: Typical external circuit

Note:

1. *Typical application circuit is to further lower the input and output ripple. It is not required for general use.
2. *Recommended component specifications are typical values. Excessive external capacitive load may cause startup problem.

Recommend component spec

FUSE	2A, slow blow
C _{IN}	10...47uF
C _{OUT}	10uF

*Recommended FUSE to be 2A slow blow, and C_{IN} to be 100uF, 100V

Circuit for EMC Enhancement

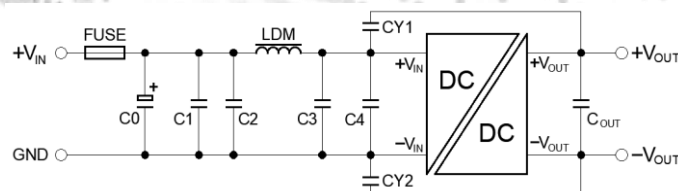


Figure 2: Circuit for EMC Enhancement

* The formulas to calculate the desired resistance of Trim resistor "R_T".

6W, DC/DC Power Converter

SDA06G



Recommended component spec

Items	LDM	C0	C1 ... C4	CY1, CY2
Spec	68uH	100uF, 200V	0.22uF, 250V	1nF, 3KV

* "Fuse" to be selected according to application needs. "C_{OUT}" refer to the same in Table 1.

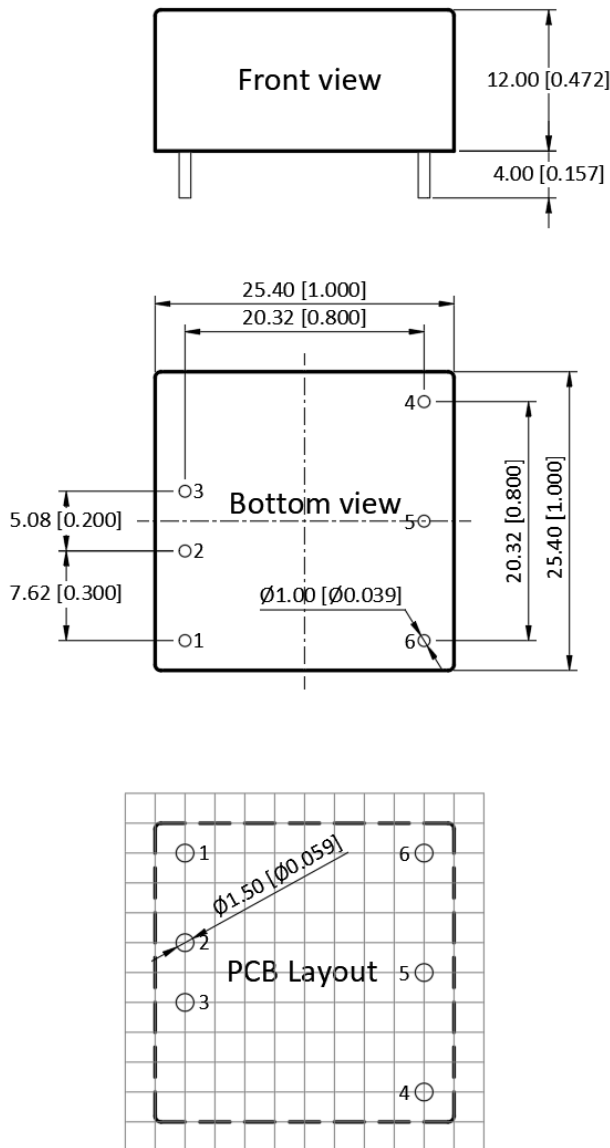
* Do not use two converters in parallel to supply higher power.

6W, DC/DC Power Converter

SDA06G



Mechanical Specifications



Pin Definition

Pin #	Single Out	Dual Out
1	No Pin	Ctrl
2	GND	GND
3	+V _{IN}	+V _{IN}
4	+V _{OUT}	+V _{OUT}
5	No Pin	0V
6	0V	-V _{OUT}

* Unless otherwise specified unit:
mm [inch]

* General tolerance: ± 0.50 [± 0.020]

* Pin thickness: ± 0.10 [± 0.004]

* Footprint grid 2.54 x 2.54 mm

6W, DC/DC Power Converter

SDA06G



Ordering information

Ordering can be done via www.summit-electronics.com or via info@summit-electronics.com. Please contact us for more information. Customisation of the product is available on request.

Technical support

For all product questions please contact us via info@summit-electronics.com

Document revision

Rev	Date	Changes
2025v0.1	29-08-2025	First issue of document